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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet	1	of	2
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Application Number	10/684,053
Filing Date	October 9, 2003
First Named Inventor	Chandan Mathur
Art Unit	2183
Examiner Name	
Attorney Docket Number	1934-12-3

U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				
DJH		EP 1 061 438 A1	12/20/00	Hewlett-Packard Company		
DJH		EP 0 945 788 A2	09/29/99	Texas Instruments Incorporated		
DJH		EP 0 945 788 A3	06/26/02	Texas Instruments Incorporated		
DJH		EP 0 694 847 A2	01/31/96	International Business Machines Corporation		

**Examiner
Signature**

/David Huisman/

Date Considered

10/15/2006

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		LECURIEUX-LAFAYETTE G: "Un Seul FPGA Dope Le Traitement D'Images", Electronique, CEP Communication, Paris, FR, no. 35, 1990, pp. 90, 101-103	
DJH		VERMEULEN F. ET AL., "Flexible Hardware Acceleration for Multimedia Oriented Microprocessors", Micro-33. Proceedings of the 33 rd Annual ACM/IEEE International Symposium on Microarchitecture. Monterey, CA, Dec. 10-13, 2000, Proceedings of the Annual ACM/IEEE International Symposium on Microarchitecture, Los Alamitos, CA: IEEE Comp. Soc. US, 10 December 2000, pp. 171-177	
DJH		International Search Report for PCT/US 03/34557, December 21, 2004	
DJH		International Search Report for PCT/US 03/34559, January 7, 2005	

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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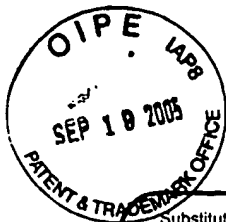
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Examiner Signature	/David Huisman/	Date Considered	10/15/2006
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DJH		SALCIC Z ET AL., "FLIX environment for generation of custom-configurable machines in FPLDs for embedded applications", Microprocessors and Microsystems, IPC Business Press Ltd. London, GB, vol. 23, no. 8-9, December 15, 1999, pp. 513-526	
DJH		International Search Report for PCT/US 03/34558, June 16, 2005	

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